This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of claims:

Claims 1-19 canceled

Claim 20 (new): A method for one of encoding and decoding a sequence of digital data, the method comprising:

providing that a portion of the sequence of digital data corresponds to a data block;

providing that the data block include a plurality of data packets;

providing that the data packets contain an identifier, with a position of each respective data packet within the associated data block being determined based on the identifier;

providing that the data packets contain information relating to a width of the data block;

providing that at least one data packet per data block contains the identifier and at least one further data packet contains the information relating to the width of the data block; and

transmitting, alternately, the identifier and the information relating to the width of the data block according to a predefinable repetition pattern, in a data field, wherein the data is respectively one of encoded and decoded in consideration of the identifier.

Claim 21 (new): A method for one of encoding and decoding a sequence of digital data as claimed in claim 20, wherein the sequence of digital data contains a sequence of progressive data.

Claim 22 (new): A method for one of encoding and decoding a sequence of digital data as claimed in claim 20, wherein the sequence of digital data contains a sequence of digital image data.

Claim 23 (new): A method for one of encoding and decoding a sequence of digital data as claimed in claim 20, wherein the data block contains redundancy information.

Claim 24 (new): A method for one of encoding and decoding a sequence of digital data as claimed in claim 20, wherein a start and an end of the data block are determined via the identifier.

Claim 25 (new): A method for one of encoding and decoding a sequence of digital data as claimed in claim 20, wherein a number of data packets containing the identifier is predefined such that every n-th data packet receives the identifier.

Claim 26 (new): A method for one of encoding and decoding a sequence of digital data as claimed in claim 20, wherein a number of data packets containing the identifier is predefined such that the data field of every n-th data packet contains the identifier and at least some of the remaining data packets respectively contain the width of the data block in their respective data fields.

Claim 27 (new): A method for one of encoding and decoding a sequence of digital data as claimed in claim 20, wherein at least two data packets contain the identifier and are every other data packet.

Claim 28 (new): A method for one of encoding and decoding a sequence of digital data as claimed in claim 20, wherein the data block is an interleaver block.

Claim 29 (new): A method for one of encoding and decoding a sequence of digital data as claimed in claim 20, wherein a sequence of the data blocks is determined.

Claim 30 (new): A method for one of encoding and decoding a sequence of digital data as claimed in claim 20, wherein a sequence of the data blocks is determined based on at least one of a time stamp and a serial number.

Claim 31 (new); A method for one of encoding and decoding a sequence of digital data as claimed in claim 20, wherein a Real-Time Transfer Protocol is used as protocol.

Claim 32 (new): A method for one of encoding and decoding a sequence of digital data as claimed in claim 20, wherein the identifier for determining the position of the data packet within the data block is a sequential number.

Claim 33 (new): A method for one of encoding and decoding a sequence of digital data as claimed in claim 31, wherein the identifier for determining the position of the data packet within the data block is determined from the sequential number of a Real-Time Transfer Protocol used.

Claim 34 (new): A method for one of encoding and decoding a sequence of digital data as claimed in claim 20, wherein an unequal error protection method is used.

Claim 35 (new): A method for one of encoding and decoding a sequence of digital data as claimed in claim 34, wherein the unequal error protection method used is a USP method.

Claim 36 (new): A system for one of encoding and decoding a sequence of digital data, comprising a processor unit which provides that a portion of the sequence of digital data is a data block, the data block include a plurality of data packets, the data packets contain an identifier based on which a position of each respective data packet within the associated data block is determined, the data packets contain information relating to a width of the data block, at least one data

packet per data block contains the identifier and at least one further data packet contains the information relating to the width of the data block, the identifier and the information relating to the width of the data block are alternately transmitted according to a predefinable repetition pattern, in a data field, and the data is respectively one of encoded and decoded in consideration of the identifier.